RLD FINAL TEST PROCEDURE   SERIAL NO: RLD1712-007   DATE: 11-FEB-2018

Test Description	PASS	N/A
Section 1: 32 STEP TIMING SEQUENCER		
Stop light shows correct first step in all modes	✔	☐
Sequence speed (measured) at 1:1 sync ratio - full range	33bpm - 356bpm	
Approx. maximum speed before dropping clock pulses - Ext Clock In	603bpm (14,472 pulses/min)	
Forward, Reverse and Alternate work in 32 Step Mode	✔	☐
Forward, Reverse and Alternate work in 16 Step Mode in pattern A & B	✔	☐
All Sync Ratios work (1:1, 1:2, 1:4)	✔	☐
All steps send triggers	✔	☐
EOP (End Of Pattern) Reset pulse works, and can be blocked	✔	☐
Pattern & Direction voltage control inputs work	✔	☐
Clock In, Pause and Reset voltage control inputs work	✔	☐
Clock Dividers show correct intervals	✔	☐
MIDI IN + Synchronisation Check	✔	☐
MIDI THRU (MIDI Clock In) - both ports if applicable	✔	☐
MIDI OUT (Internal Clock) - both ports if applicable	✔	☐
DIN SYNC OUT (optional) - only if configured	☐	✔
Section 2: 12 STEP CV SEQUENCER		
All steps receive triggers	✔	☐
All modes work (FWD, REV, ALT, MUTE, SKIP, VAS, BROWNIAN, LAST)	✔	☐
Gate Outs (1, 2 & 3)	✔	☐
Gate Voltage	8.86V	
CV 1, Including HOLD and SLEW	✔	☐
CV 1 KILL SLEW	✔	☐
CV 2, Including HOLD and SLEW	✔	☐
Manual Step and Reset - during sequence	✔	☐
Manual Step and Reset in isolation (all triggers off, EOP blocked)	✔	☐
Section 3: FINISH		
Visual Check	✔	☐
Signed:		